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## **U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	Α	US-			
	В	US-			
	C	US-			
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	Е	US-			
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	Н	US-			
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	L	US-			
	М	US-			

## **FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N	EP 0 822 596 A2	02-1998	Europe	Amerasekera, E. Ajith	H01L 27/02
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	P					
	Q					
	R					
	s					
	Т					

## **NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf, S, "Silicon Processing for the VZLSI Era", Volume 2 - Process Integration, Lattice Press, Sunset beach, California 90742 (ISBN: 0-961672-4-5) (1990), pages 658-661.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).) Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.